

LOGIC CIRCUIT WITH VARIABLE INTERNAL POLARITIES

Abstract of the Disclosure

Operation of a logic circuit for performing a desired logic function is scrambled. Logic gates and/or transistors are provided in the logic circuit so
5 that the logic function is performed in at least two different ways. The way in which the logic function is performed is determined by the value of a function selection signal applied to the logic circuit. The function selection signal is random and is applied to
10 the logic circuit, and the function selection signal is refreshed at determined instants for scrambling operation of the logic circuit. For identical data applied at the input of the logic circuit and for different values of the function selection signal, the
15 polarities of certain internal nodes of the logic circuit and/or the current consumption of the logic circuit are not identical.